

**U.S. UTILITY Patent Application**

PATENT NUMBER

**O.I.P.E.**

**PATENT DATE**

**SCANNE**

## Q.A.

## APPLICANTS

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**TITLE**

Semiconductor structure, semiconductor device, communicating device, integrated circuit, and process for fabricating the same

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="border-bottom: 1px solid black; margin-bottom: 10px;">             _____ (Assistant Examiner) _____ (Date)           </div> <div style="border-bottom: 1px solid black; margin-bottom: 10px;">             _____ (Primary Examiner) _____ (Date)           </div> <div style="border-bottom: 1px solid black;">             _____ (Legal Instruments Examiner) _____ (Date)           </div>			<b>NOTICE OF ALLOWANCE MAILED</b>	
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